REMARKS

This Amendment serves as a submission accompanying a Request for Continued Examination (RCE) being filed herewith.

Claims 1 and 7 have been amended. Specifically, claim 7 was amended to correct an informality. No new matter has been added. Claims 1 to 9 and 18 are now pending. Applicants respectfully request reconsideration of the present application in view of this response.

Claims 1 to 9 and 18 were rejected under 35 U.S.C. § 102(b) as allegedly anticipated by U.S. Patent No. 5,396,492 to Lien ("Lien reference").

Applicants respectfully submit that the Lien reference does not identically describe or suggest each and every feature of the claims, as required for anticipation.

The Lien reference recites an adaptive clock recovery arrangement for deriving a synchronous clock from an asynchronous, packet stream such as an asynchronous transfer mode (ATM) cell stream. The Lien reference further recites that a deviation in the magnitude of information stored in a first-in-first-out memory is continually monitored, and the synchronous clock frequency, referred to as the adaptive line clock frequency, is adjusted in a plurality of modes, under the control of a processor. According to the Lien reference, that adjustment is made in response to a detected increasing condition of the monitored deviation. The adjustments are open-loop adjustments made without continually adjusting the adaptive line clock frequency based on the monitored deviation. The Lien reference refers to its differences from conventional PLL arrangements in that it includes a reduction in damping because the open-loop adjustments result in a rapid frequency correction with perfect or nearly perfect deadbeat damping, *i.e.*, without the frequency oscillations that continue after the correct frequency is reached in closed-loop arrangements.

Instead, claim 1 of the present invention concerns a device for receiving data transmitted using asynchronous data transmission technology. The device includes a data-independent clock signal and a memory device, and stores the received data for the required period of time in order to compensate for transmission delays. The clock signal is sent to the memory device for readout of the data. The Lien reference does not address this situation and instead focuses on the increasing condition of the monitored deviation, and effects a damping of the system. *In addition, the Lien reference does not identically describe that the device stores the received data for the required period of time such that a period between two disturbances is made long that any effect of the two disturbances is reduced.* Accordingly, Applicants respectfully submit that claim 1 and its dependent claims 2 to 9 and 18 are allowable.

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Accordingly, Applicants respectfully submit that claims 1 to 9 and 18 are allowable. Applicants respectfully request withdrawal of the rejection under 35 U.S.C. § 102(b) of those claims.

CONCLUSION

In view of the foregoing, it is believed that the rejection of the claims under 35 U.S.C. § 102(b) has been overcome, and that claims 1 to 9 and 18 are allowable. It is therefore respectfully requested that the rejections be withdrawn, and that the present application issue as early as possible.

In efforts to further the prosecution of this application, the undersigned would be happy to discuss the above application with the Examiner.

Respectfully submitted,

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